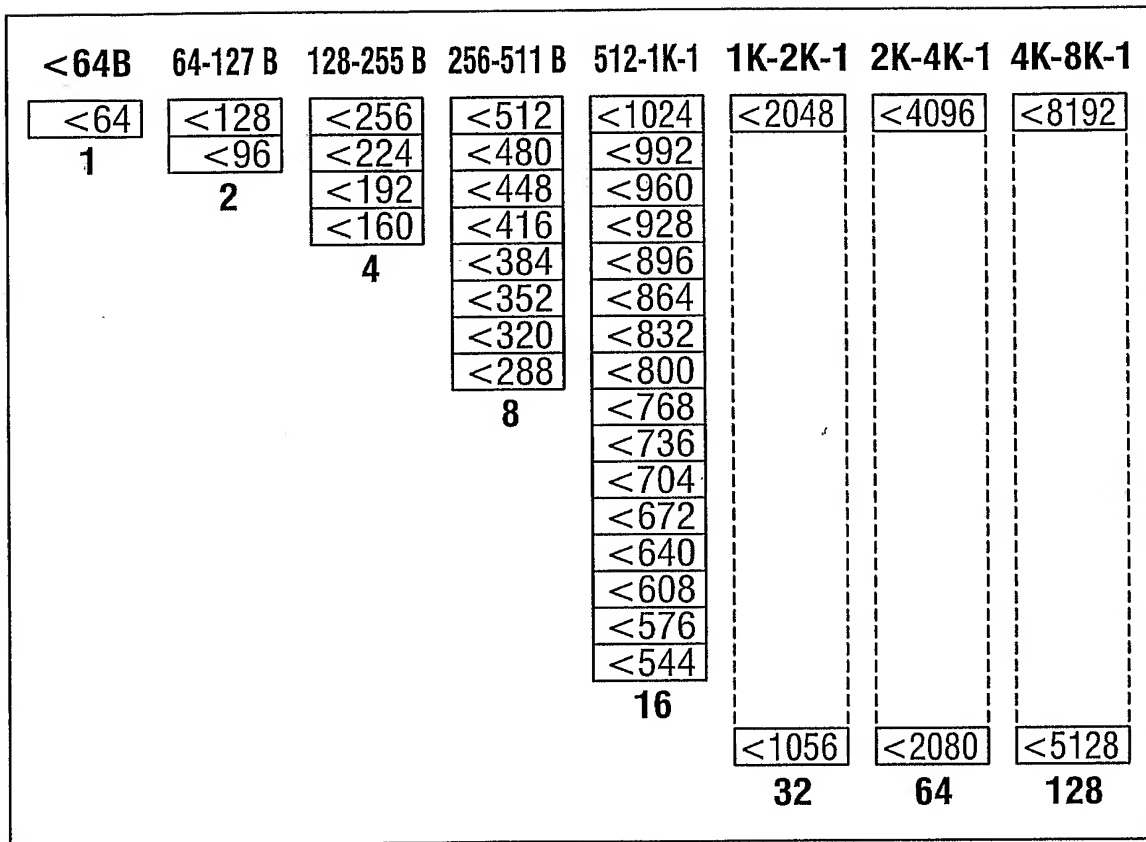
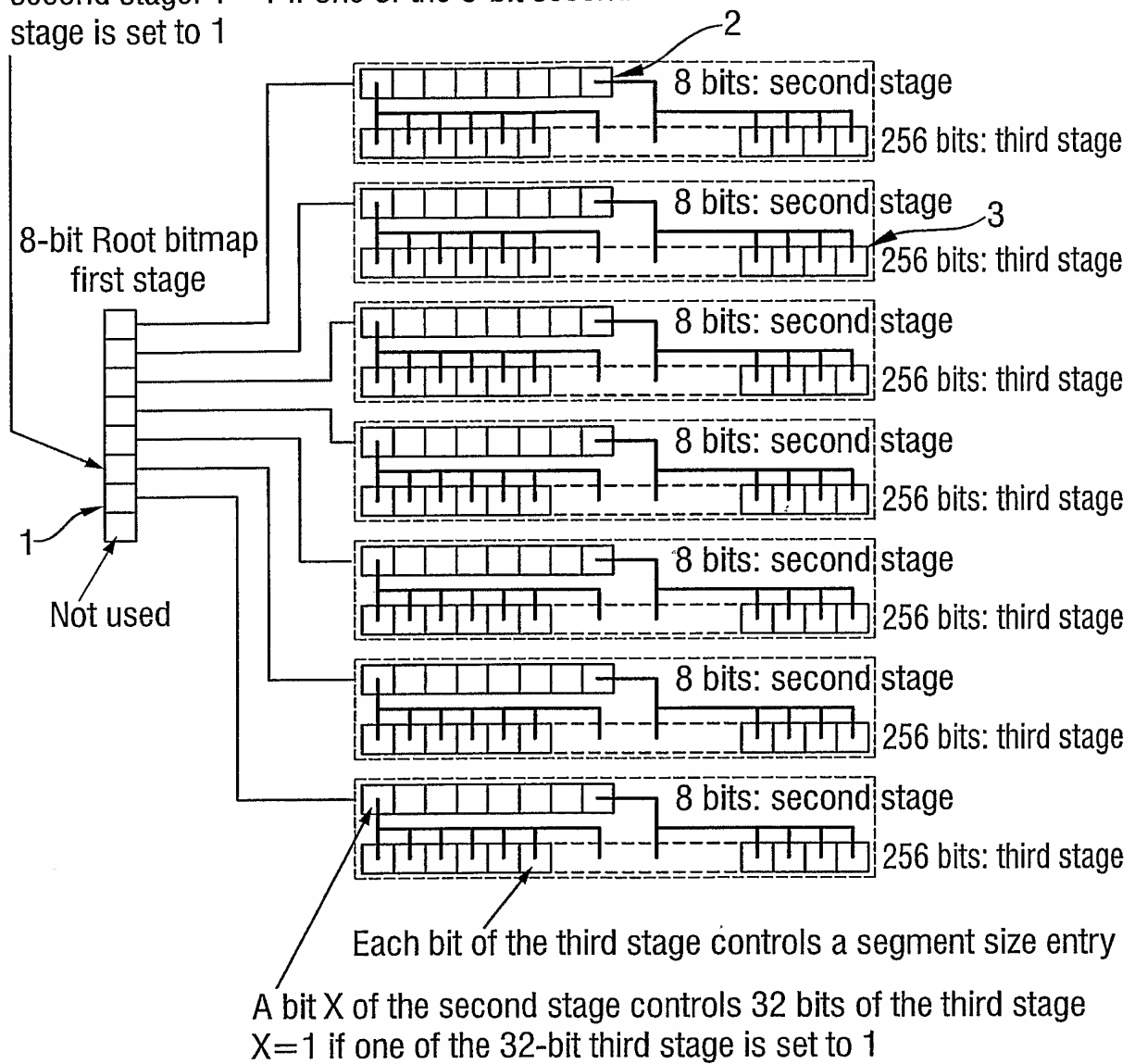


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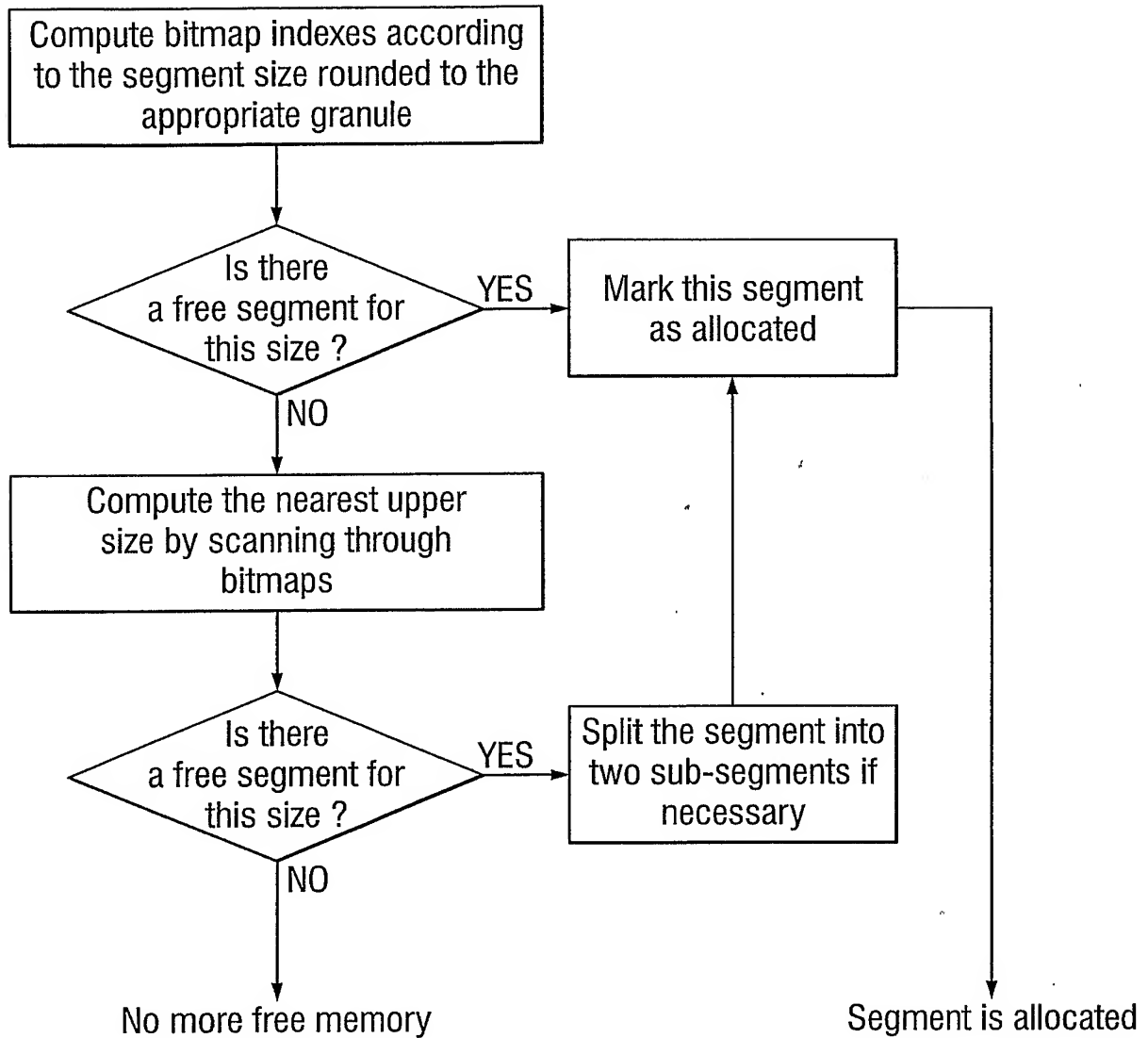
***Fig. 1***

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A bit Y of the first stage controls 8 bits of the second stage. Y=1 if one of the 8-bit second stage is set to 1

**Fig. 2**

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***Fig. 3***

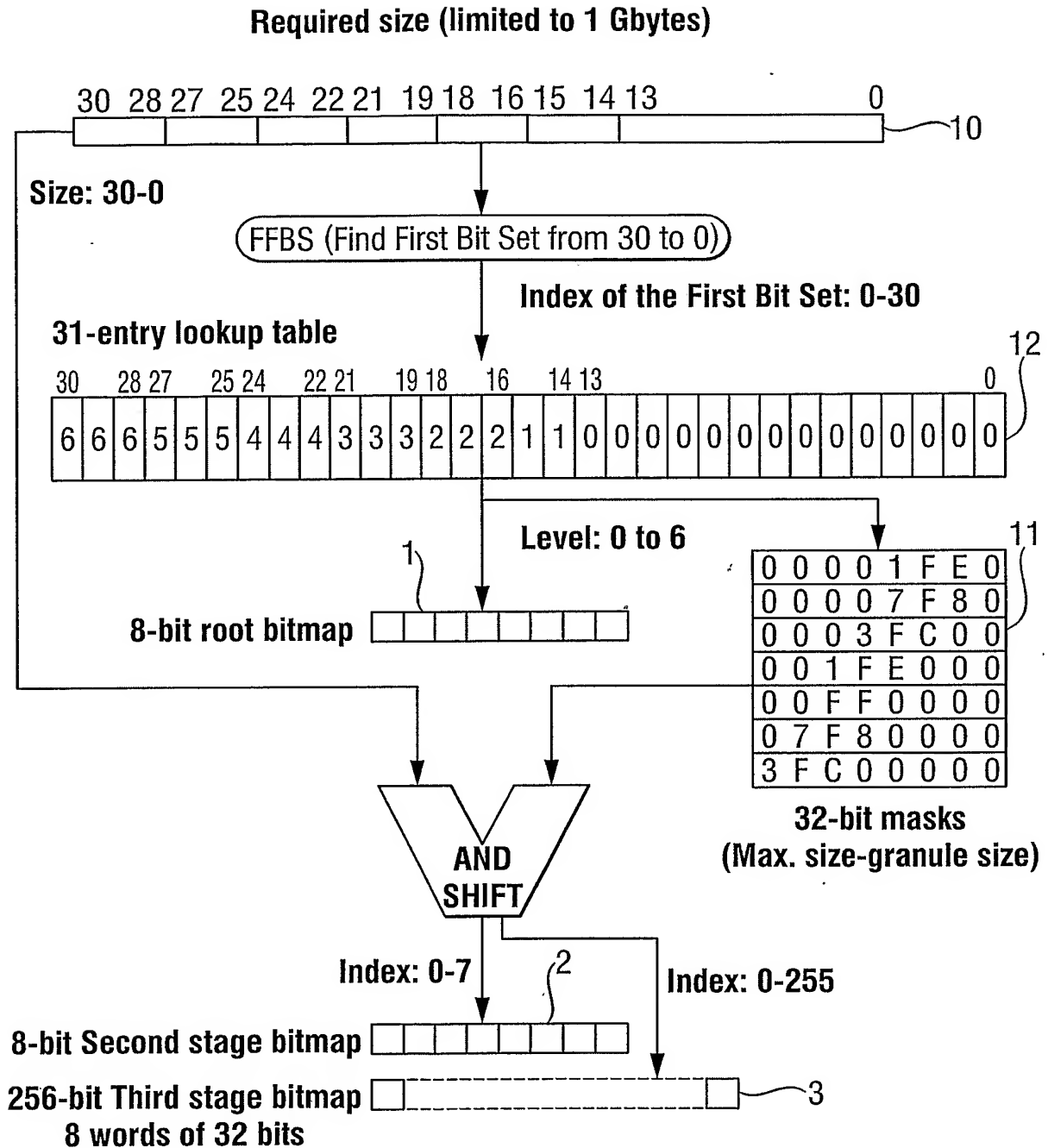
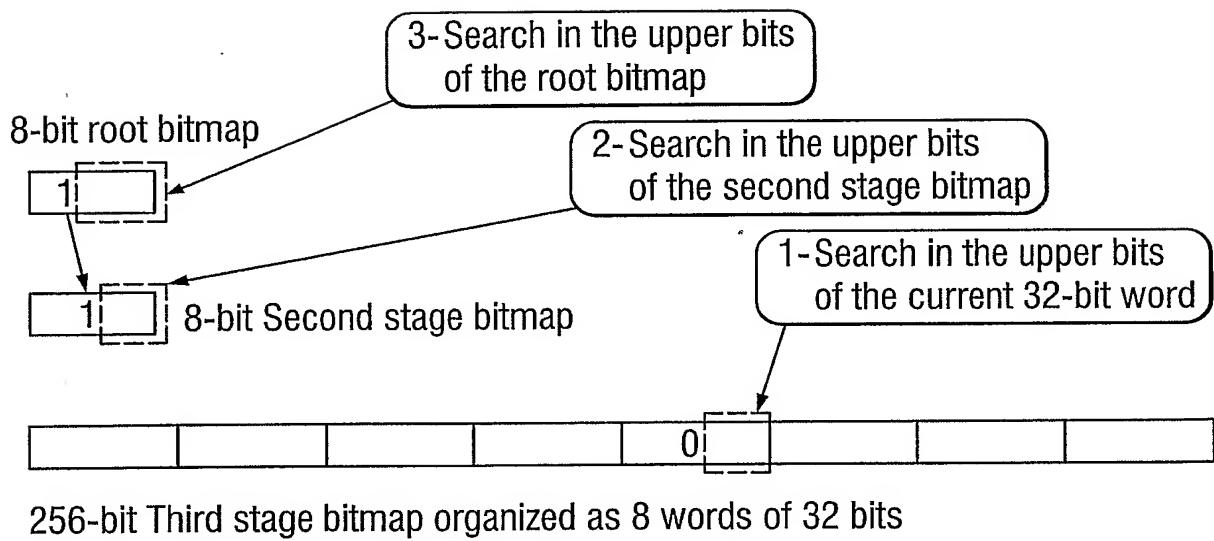
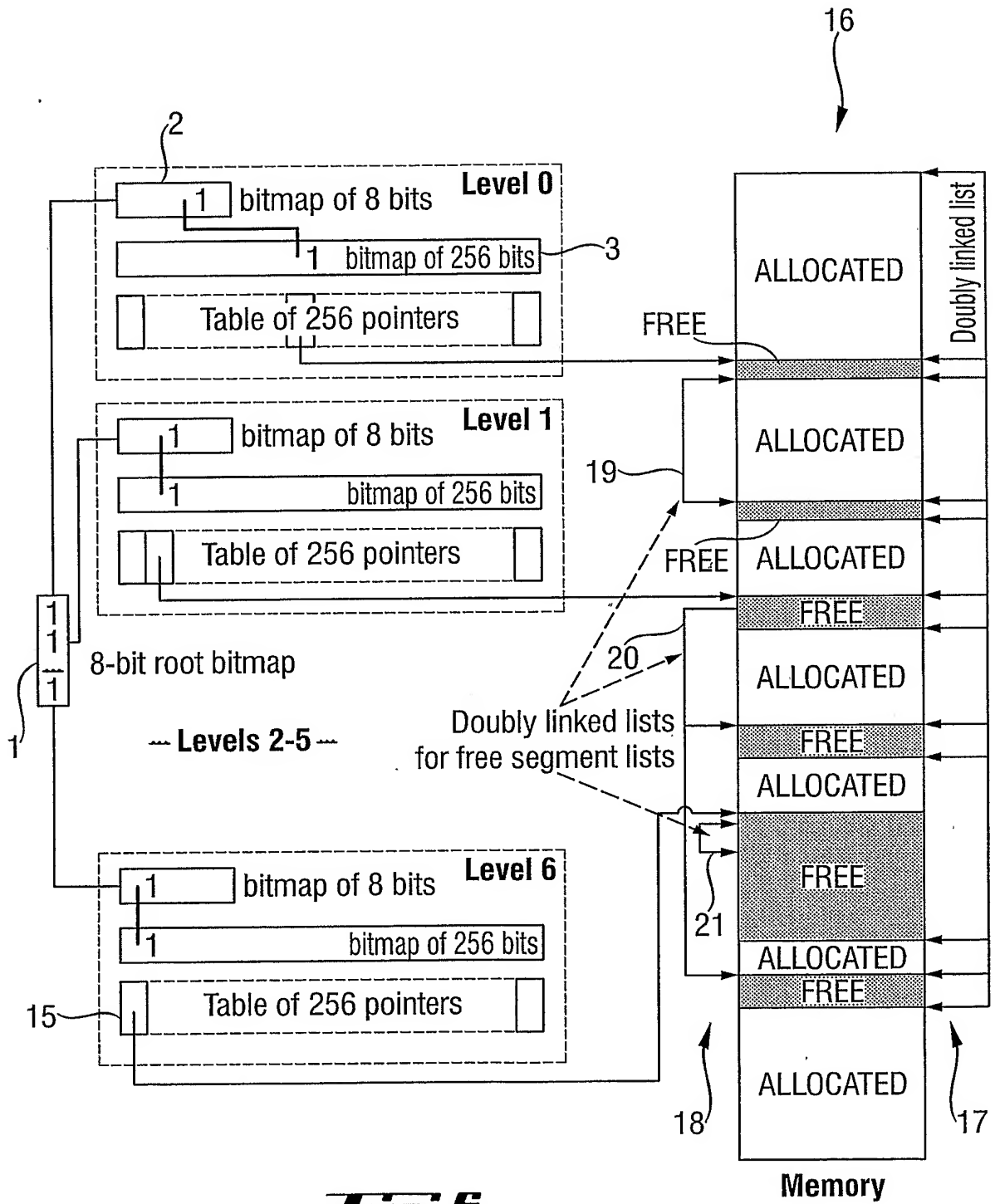


Fig. 4

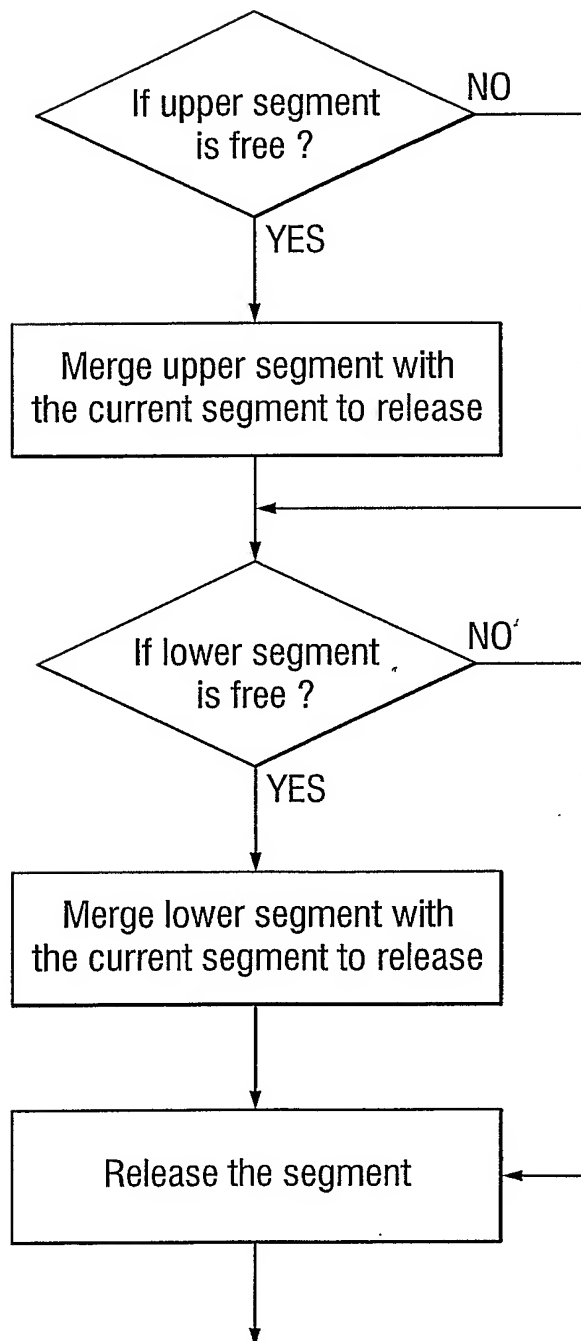
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***Fig. 5***

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***Fig. 7***

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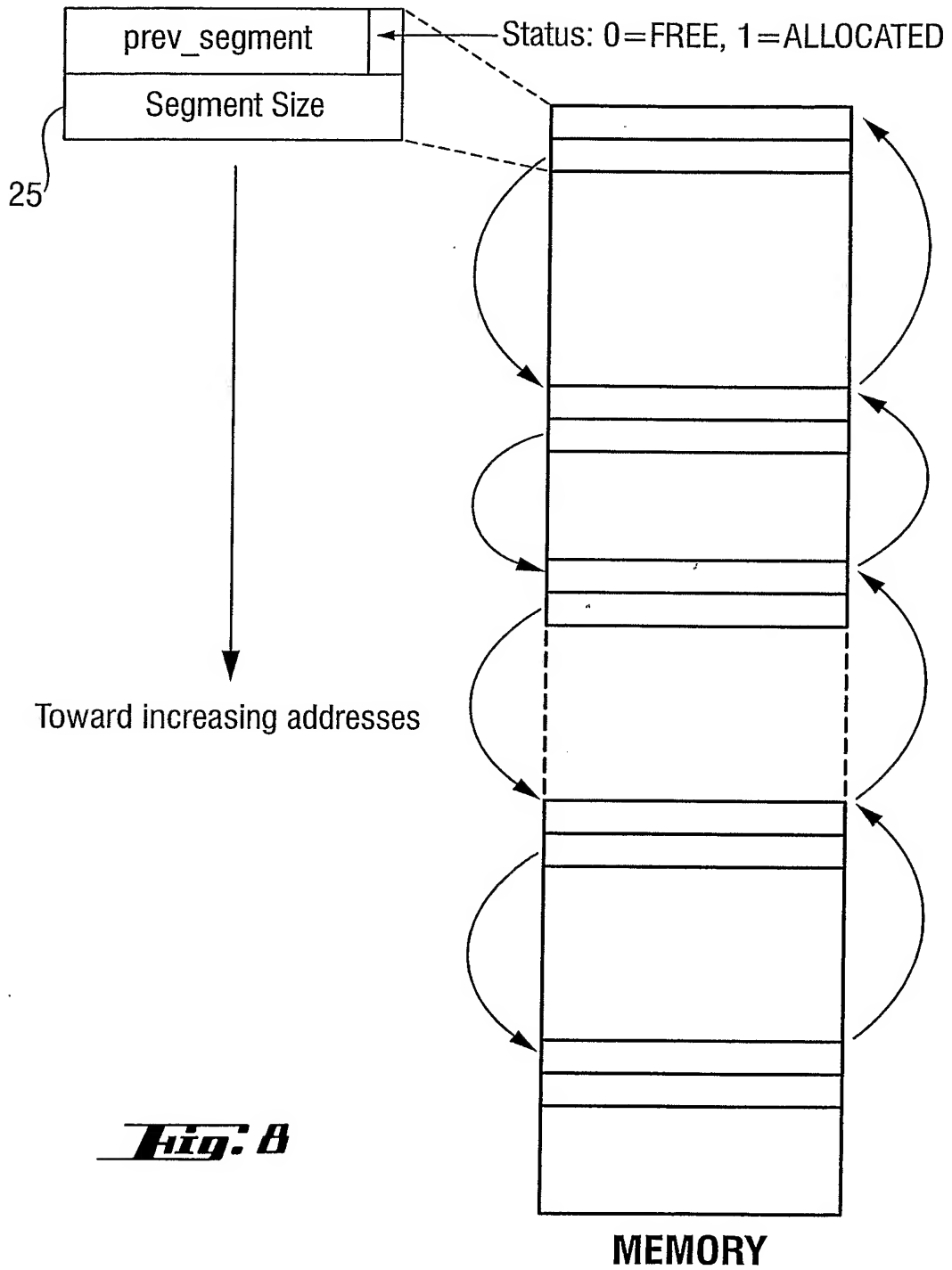
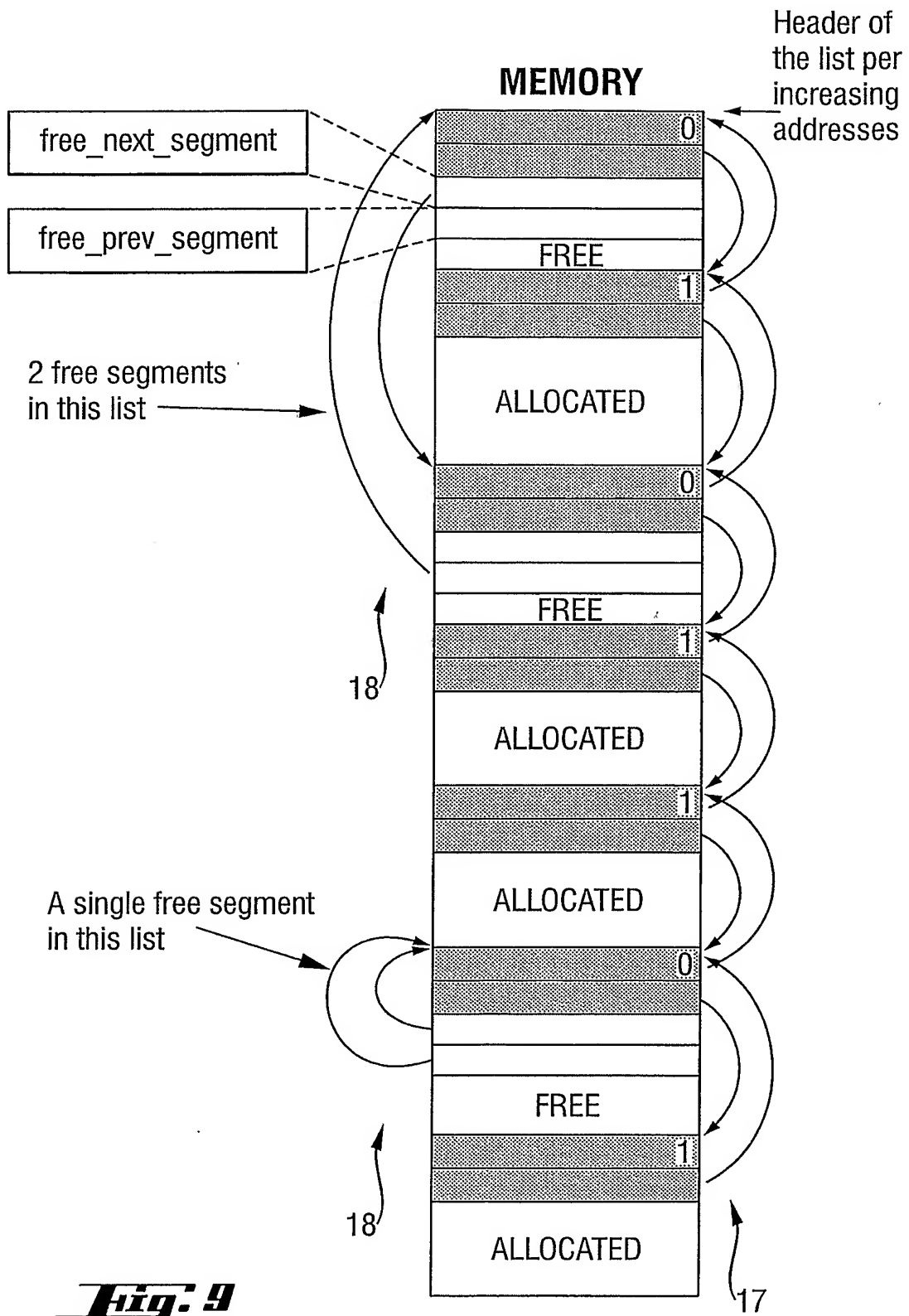


Fig. 8

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**Fig. 9**